Description – Input Capture

The example demonstrated in class has been provided as the starter project.

Make the following changes:

(i) Change the capture pin to P6.6

(ii) Change the ACLK divider to generate timer overflows every 0.5 sec

(iii) Add an ISR to keep track of timer overflow counts

(iii) Modify the formula for calculating elapsed time to take into account overflows

Execute the project and test by providing two rising edges on P6.6 with a time span of 5 secs approx.

Elapsed time can be displayed in debug mode or using UART

Deliverables:

(i) Zipped project

(ii) Screenshot of result

(iii) Checkoff sheet